

FIG. 1A

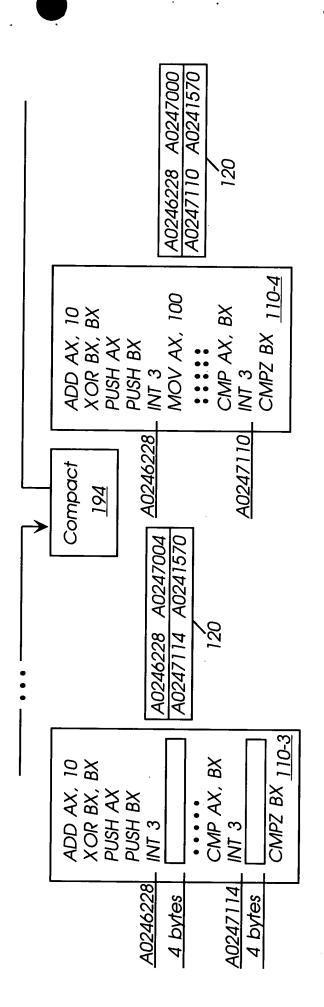


FIG. 1B

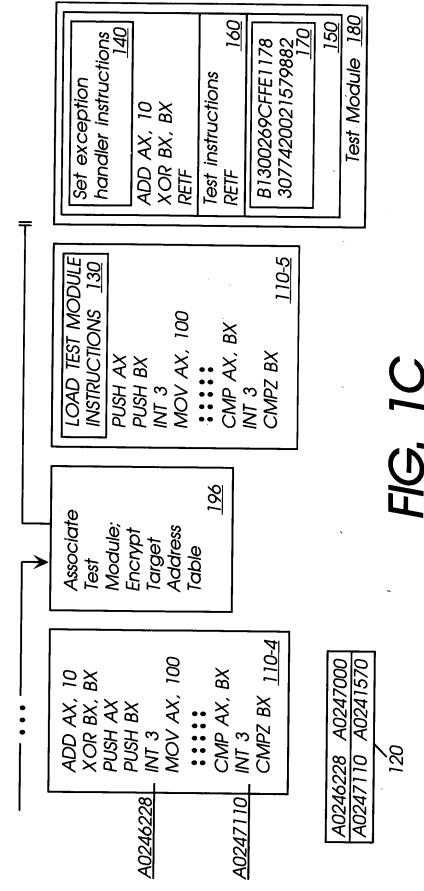


FIG. 1C

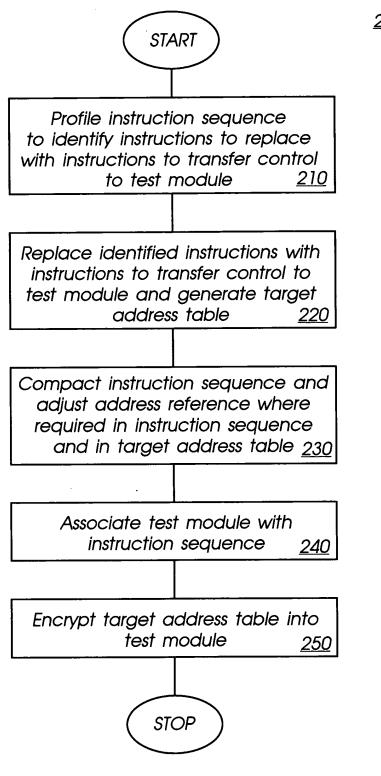
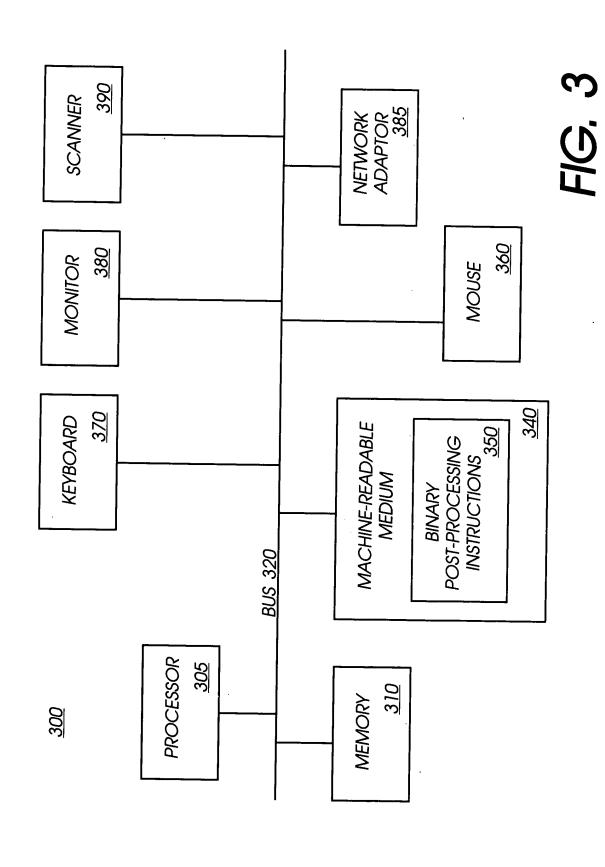


FIG. 2



Machine-Readable Storage Medium

> Binary Post-Processing Instructions

> > Instruction to identify instruction in instruction sequence to replace

<u>420</u>

Instructions to replace identified instructions with instructions to transfer control to test module 430

Instructions to generate target address table 440

Instructions to compact instruction sequence and adjust address reference in instruction sequence and target address table 450

Instructions to associate test module with instruction sequence 460

Instructions to encrypt target address table into test module

<u>410</u>

*470* 

400

MACHINE-READABLE STORAGE MEDUIM

> LOAD TEST MODULE INSTRUCTIONS 510 PUSH AX PUSH BX INT 3 MOV AX, 100

CMP AX, BX INT 3 CMPZ BX

<u>520</u>

Set exception handler instructions 540

ADD AX, 10 XOR BX, BX RETF

Test instructions RETF

B1300269CFFE1178 3077420021579882 570

*550* 

560

Test Module 530

*500* 

FIG. 5